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Docket No.: P2001,0349

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

By: 

Date: January <sup>12</sup>~~11~~, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/717,337 Confirmation No: 8258  
Applicant : Alexander Benedix, et al.  
Filed : November 19, 2003  
Title : Method for Determining the Optimum Access Strategy  
Art Unit : 2186  
Examiner : Samuel A. Dillon  
Docket No. : P2001,0349  
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are cited herewith:

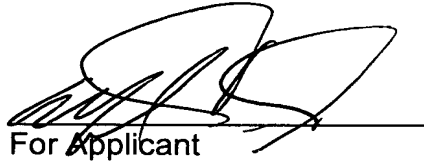
MoSys Incorporated: "Application Note - Memory Controller Design - Using MoSys High-Speed SGRAM", AN-SG01 Rev. 1.4 - 9/1/1998, Sunnyvale, CA, pp. 1-14.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.



Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicant

Alfred K. Dassler  
52,794

Date: January 11, 2006

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2001,0349	Applic. No. 10/717,337
	Applicant Alexander Benedix, et al.	
	Filing Date November 19, 2003	Group Art Unit 2186

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

## FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	MoSys Incorporated: "Application Note - Memory Controller Design - Using MoSys High-Speed SGRAM", AN-SG01 Rev. 1.4 - 9/1/1998, Sunnyvale, CA, pp. 1-14.
	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.